

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10574117	
	Filing Date		2006-03-30	
	First Named Inventor	Rao Rajashekhar		
	Art Unit			
	Examiner Name			
	Attorney Docket Number		I438.113.101/IF02P041WOUS	

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	1	"A Pseudo Multi-Bank DRAM with Categorized Access Sequence", Shinichiro Shiratake, et al., 1999 Symposium on VLSI Circuits Digest of Technical Papers	<input type="checkbox"/>

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☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.

☒ None

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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Steven E. Dicke/	Date (YYYY-MM-DD)	2007-08-10
Name/Print	Steven E. Dicke	Registration Number	38,431

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